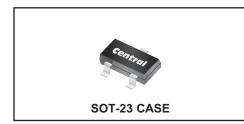


SURFACE MOUNT VERY LOW VCE(SAT) NPN SILICON TRANSISTOR



# **APPLICATIONS:**

- DC/DC Converters
- Voltage Clamping
- Protection Circuits

Battery powered	Cell Phones, Pagers,
Digital Cameras,	PDAs, Laptops, etc.

MAXIMUM RATINGS: (T <sub>A</sub> =25°C)	SYMBOL	
Collector-Base Voltage	V <sub>CBO</sub>	80
Collector-Emitter Voltage	V <sub>CEO</sub>	60
Emitter-Base Voltage	V <sub>EBO</sub>	5.0
Continuous Collector Current	IC	1.0
Peak Collector Current	ICM	2.0
Continuous Base Current	۱ <sub>B</sub>	300
Power Dissipation	PD	350
Operating and Storage Junction Temperature	TJ, T <sub>stg</sub>	-65 to +150
Thermal Resistance	$\Theta_{JA}$	357

### ELECTRICAL CHARACTERISTICS: (TA=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
ICBO	V <sub>CB</sub> =60V		100	nA
IEBO	V <sub>EB</sub> =5.0V		100	nA
BVCBO	Ι <sub>C</sub> =100μΑ	80		V
BVCEO	I <sub>C</sub> =10mA	60		V
BVEBO	Ι <sub>Ε</sub> =100μΑ	5.0		V
V <sub>CE(SAT)</sub>	I <sub>C</sub> =100mA, I <sub>B</sub> =1.0mA		0.115	V
V <sub>CE</sub> (SAT)	I <sub>C</sub> =500mA, I <sub>B</sub> =50mA		0.15	V
V <sub>CE</sub> (SAT)	I <sub>C</sub> =1.0A, I <sub>B</sub> =100mA		0.28	V
V <sub>BE(SAT)</sub>	I <sub>C</sub> =1.0A, I <sub>B</sub> =50mA		1.1	V
V <sub>BE(ON)</sub>	V <sub>CE</sub> =5.0V, I <sub>C</sub> =1.0A		0.9	V
h <sub>FE</sub> `´	V <sub>CE</sub> =5.0V, I <sub>C</sub> =1.0mA	200		
h <sub>FE</sub>	V <sub>CE</sub> =5.0V, I <sub>C</sub> =500mA	200		
h <sub>FE</sub>	V <sub>CE</sub> =5.0V, I <sub>C</sub> =1.0A	100		
fT	V <sub>CE</sub> =10V, I <sub>C</sub> =50mA	150		MHz
Cob	V <sub>CB</sub> =10V, I <sub>E</sub> =0, f=1.0MHz		10	pF
				R1 (1-February 2010)



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UNITS V V V А А mΑ mW °C °C/W

# **DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CMPT3820 is a very low  $V_{\mbox{CE}(\mbox{SAT})}$  NPN Transistor, designed for applications where size and efficiency are prime requirements. Packaged in an industry standard SOT-23, this device brings updated electrical specifications and characteristics suitable for the most demanding designs.

## **MARKING CODE: 38C**

### FEATURES:

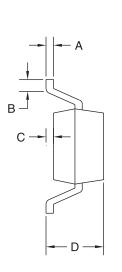
- Device is Halogen Free by design
- High Current (IC=1.0A)
- V<sub>CE(SAT)</sub>=0.28V MAX @ I<sub>C</sub>=1.0A
- SOT-23 surface mount package
- Complementary PNP device CMPT7820

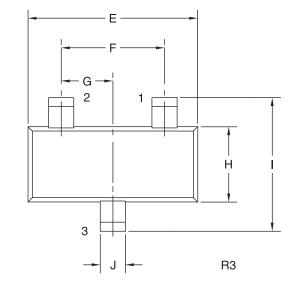


CMPT3820

# SURFACE MOUNT VERY LOW VCE(SAT) NPN SILICON TRANSISTOR

# SOT-23 CASE - MECHANICAL OUTLINE





LEAD CODE:
1) Base

- 2) Emitter
  3) Collector
- MARKING CODE: 38C

DIMENSIONS						
	INCHES		MILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX		
А	0.003	0.007	0.08	0.18		
В	0.006	-	0.15	-		
С	-	0.005	-	0.13		
D	0.035	0.043	0.89	1.09		
E	0.110	0.120	2.80	3.05		
F	0.075		1.90			
G	0.037		0.95			
Н	0.047	0.055	1.19	1.40		
	0.083	0.098	2.10	2.49		
J	0.014	0.020	0.35	0.50		

SOT-23 (REV: R3)

R1 (1-February 2010)

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# OUTSTANDING SUPPORT AND SUPERIOR SERVICES

#### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options

### DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- · Environmental regulation compliance
- Customer specific screening
- · Up-screening capabilities

· Custom product packing

Custom bar coding for shipments

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

## REQUESTING PRODUCT PLATING

- If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when 1. ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix " PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

## CONTACT US

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